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APPLICATION FOR UNITED STATES LETTERS PATENT

Integrated Services Hub Self Configuration

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TITLE OF THE INVENTION

Integrated Services Hub Self Configuration

CROSS-REFERENCE TO RELATED APPLICATIONS

5 Not Applicable.

STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR

DEVELOPMENT

Not Applicable.

REFERENCE TO A MICROFICHE APPENDIX

10 Not Applicable.

BACKGROUND OF THE INVENTION

The present invention relates to a method for initialization of a customer premises telecommunications hub, and more particularly to a method of automatically downloading configuration and binary files through the Internet.

15 Traditionally, telephony communications within the United States were handled by the public switched telecommunications network (PSTN). The PSTN can be characterized as a network designed for voice communications, primarily on a circuit-switched basis, with full interconnection among individual networks. The PSTN network is largely analog at the local loop level, digital at the

20 backbone level, and generally provisioned on a wireline, rather than a wireless, basis. The PSTN includes switches that route communications between end users. Circuit switches are the devices that establish connectivity between circuits through an internal switching matrix. Circuit switches set connections

between circuits through the establishment of a talk path or transmission path. The connection and the associated bandwidth are provided temporarily, continuously, and exclusively for the duration of the session, or call. While developed to support voice communications, circuit switches can support any form of information transfer (e.g., data and video communications).

5 In a traditional PSTN environment, circuit switches include central office (CO) exchanges, tandem exchanges, access tandem exchanges, and international gateway facilities. Central offices, also known as exchanges, provide local access services to end users via local loop connections within a relatively small area of geography known as an exchange area. In other words, the CO provides the ability for a subscriber within that neighborhood to connect to another subscriber within that neighborhood. Central offices, also known as end offices, reside at the terminal ends of the network. In other words, COs are the first point of entry into the PSTN and the last point of exit. They are also known as class 5 offices, the lowest class in the switching hierarchy. A class 5 telephone switch communicates with an analog telephone using the analog telephony signals in the well-known analog format. The class 5 telephone switch provides power to the telephone; detects off-hook status of the telephone and provides a dial tone in response; detects dual-tone multi-frequency signals from the caller and initiates a call in the network; plays a ringback tone to the caller when the far-end telephone is ringing; plays a busy tone to the caller when the far-end telephone is busy; provides ring current to the telephone on incoming

calls; and provides traditional telephone services such as call waiting, call forwarding, caller ID, etc.

In an effort to increase the amount and speed of information transmitted across networks, the telecommunications industry is shifting toward broadband

5 packet networks which are designed to carry a variety of services such as voice, data, and video. For example, asynchronous transfer mode (ATM) networks have been developed to provide broadband transport and switching capability between local area networks (LANs) and wide area networks (WANs). The Sprint ION network is a broadband network that is capable of delivering a variety

10 of services such as voice, data, and video to an end user at a residential or business location. The Sprint ION network has a wide area IP/ATM or ATM backbone that is connected to a plurality of local loops via multiplexors. Each local loop carries ATM over ADSL (asymmetric digital subscriber line) traffic to and from a plurality of integrated service hubs (ISHs), which may be at either

15 residential or business locations.

An ISH is a hardware component that links business or residential user devices such as telephones and computers to the broadband, wide area network through a plurality of user interfaces and at least one network interface. A suitable ISH is described in co-pending U.S. Pat. App. No. 09/226,575 entitled "Multi-Services Communications Device," filed on January 7, 1999 (Sprint docket number 1246), which is incorporated by reference herein in its entirety. The network interface typically is a broadband network interface such as ADSL, T1, or HDSL-2. Examples of user interfaces include telephone interfaces such as

plain old telephone system (POTS) ports for connecting telephones, fax machines, modems, and the like to the ISH; computer interfaces such as Ethernet ports for connecting computers and local area networks to the ISH; and video ports such as RCA jacks for connecting video players, recorders, monitors, and the like to the ISH.

5 In providing telephony services over a broadband network, the ISH communicates with a service manager. This connection between the telephone and the network element is typically an ATM connection, which is much different than the traditional analog line to the local switch. ATM connections usually do 10 not support analog telephony signals, such as off-hook, dial tone, and busy signals. Therefore, the ISH must provide many of the telephony functions traditionally provided by the telephone provider central office such as detect off-hook conditions, on-hook connections, and digits as well as provide the telephones with dial tone, ring voltage (sometimes referred to as ring current), 15 ringback, and busy signals. The terms off-hook and off-hook condition as used herein are generic terms meaning that a user device (whether telephone, facsimile machine, modem, etc.) connected to a telephone line is attempting to access and use the line.

The ISH includes a processing core or central processing unit, CPU, 20 which controls these functions. It must exchange data with numerous peripheral devices within and external to the ISH itself. As with any other CPU, it must have appropriate software in order to perform the many required functions. As the controller for the ISH, it requires configuration files to deal with the various

interfaces for the POTS ports, Ethernet ports, etc. However, the necessary configuration and binary files are not known until the ISH is installed in a customer premises, such as a private residence. It is not practical to expect or require individual customers to know how to configure the ISH after it is physically installed. It would be quite expensive to have a trained service technician visit each residence for initializing the system, i.e. loading the necessary software. There is a need for a method for automatically initializing an ISH when it is installed and powered up for the first time.

BRIEF SUMMARY OF THE INVENTION

The present invention provides a method for operating an integrated services hub so that upon installation it automatically initializes itself by locating and downloading the necessary configuration and binary files. Upon first power up, the ISH performs a multistep process. It first issues a DHCP request to a central office, CO, DHCP server. The DHCP server response includes the name of an appropriate configuration file, a domain name referring to a list of configuration file servers where the configuration file is stored and a list of IP addresses for domain name servers which can provide IP addresses for the configuration file servers. The ISH sends a request to a domain name server for the IP addresses of the configuration file servers. The ISH then sends a request to a configuration file server for the configuration file, downloads and parses the file from which it learns the name of the appropriate binary code file. It then requests, receives and downloads the binary code into flash memory. When the

process has been successfully completed the ISH is properly configured and goes into the run state.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a block diagram of an integrated services hub, ISH, according 5 to the present invention; and,

Figure 2 is a flow chart illustrating the automatic downloading of configuration and binary files.

DETAILED DESCRIPTION OF THE INVENTION

Referring to Fig. 1, integrated services hub (ISH) 10 comprises plain old 10 telephone system (POTS) interface 15, local area network (LAN) interface 20, and wide area network (WAN) interface 25. POTS interface 15 and LAN interface 20 provide user interfaces for the ISH. WAN interface 25 provides the ISH with an interface to a central office, CO, 30 having a broadband packet network such as Sprint's ION network. The CO 30 provides the wide area 15 connection to the Wide Area Network (WAN), i.e. the Internet. In describing the ISH, the components are generally described in carrying out the appropriate function to transmit data from the user interfaces to the WAN, and it should be understood that these components in general perform the reverse function when receiving data from the WAN and transmitting it to the user interfaces.

20 Telephones 32 are connected by telephone lines 34 to POTS interface 15 by telephone jacks 36, for example industry standard RJ-11 jacks. While four telephones are shown in Fig. 1, any desirable number of telephones may be employed provided that the equipment is appropriately sized to accommodate

that number. Telephones 32 are conventional communications devices that typically use the analog telephony format, and similar devices may be substituted such as facsimile machines or modems. Digital format telephones could also be used, with the understanding that the specific circuitry involved would change 5 while providing equivalent functions (for example, detecting hook transitions).

Telephone lines 34 are any medium operational to carry telephony signals, and are typically twisted pairs of copper wires.

In addition to telephone jacks 36, POTS interface 15 further comprises subscriber line interface circuits (SLICs) 38, subscriber line access circuit (SLAC) 10 40, and digital signal processor (DSP) 45. SLICs 38 are connected to telephone jacks 36 by connections 37 on a one-to-one basis. In other words, each telephone connected to the ISH by a telephone jack has a separate and corresponding SLIC. Alternatively, SLICs may incorporate more than one channel each, that is to say a SLIC may be connected to more than one 15 telephone line. The SLICs convert relatively high power, two-line analog signals (also referred to as a tip and ring phone circuit) received from the telephone into relatively low power, four-line analog signals (also referred to as a transmit signal circuit and a receive signal circuit). Furthermore, the SLIC serves as the amplifier that drives power (i.e., voltage and current) onto its corresponding 20 telephone line. Examples of suitable SLICs are integrated circuit chip numbers AM79R79 and AM79R241 produced by Legerity, the former communications division of Advanced Micro Devices (AMD), Inc.

SLAC 40 is connected to SLICs 38 by connections 39. Generally, a SLAC performs three functions: line supervision, signal filtering and manipulation, and analog/digital conversions. The SLAC supervises the lines (and the SLIC corresponding to each line), which includes execution of various protocols for

5 detecting and recognizing an off-hook condition. While a preferred embodiment shown in Fig. 1 uses a SLAC to perform line supervision, SLICs are available that perform line supervision functions, and such enhanced SLICs having integral hook transition monitoring capability could be used. Signal filtering and manipulation is used to conform signals to various industrial or regional/national

10 telephony standards. The SLAC converts four-line analog signals received from the SLICs into four-line digital signals. Thus, the SLAC is an analog to digital (A/D) and digital to analog (D/A) converter, which is also referred to respectively as coding and decoding (i.e., a CODEC). Preferably, the SLAC codes and decodes in compliance with the International Telecommunications Union (ITU) 15 G.711 standard. Typically, a SLAC manages a fixed number of SLICs, and thus the number of SLACs is determined by the number of SLICs. Examples of suitable SLACs are integrated circuit chip numbers AM79Q021 and AM79Q2243 produced by Advanced Micro Devices (AMD), Inc., which are a quad-devices designed to manage four SLICs.

20 SLAC 40 transmits and receives control data such as line status and signaling to the central processing unit (CPU) 50 by connection 41. The CPU 50 controls the overall function of the ISH and has initialization, management, configuration, and control responsibilities for all of the components and interfaces

comprising the ISH. Control software, i.e. the binary code, is stored in flash memory 54. Upon start up or reset of the ISH, the CPU 50 loads the control software into RAM from which it is executed by the CPU 50. Upon execution of the control software, the CPU 50 interacts with various memory mapped peripherals (for example, WAN interface 25). The CPU 50 control software includes a) a POTS control module 51 that controls the telephony functions during operation of the ISH under primary power; b) a polling control module 52 that controls the POTS telephony functions when primary power fails; c) a power supply control module 83 which monitors, either continuously or periodically, the power supply; and d) ATM control module 55 that controls the communications with the network. An example of a suitable CPU is integrated circuit chip number MPC8260 Power QUICC II Communications Processor produced by Motorola, Inc.

Digital signal processor (DSP) 45 compresses (i.e., packetizes) data, typically voice data, received from SLAC 40 by connection 46. DSP 45 transmits compressed data to CPU 50 via connection 47 for transmission to CO 30 by ATM control module 55. An example of a suitable DSP is integrated circuit chip number TMS320VC5402 produced by Texas Instruments, Inc. ATM control module 55 provides the ATM and AAL (ATM adaptation layer) layers of the ATM protocol, which are necessary for ATM-formatted communications with CO 30. In a preferred embodiment, ATM control module 55 is a software module on CPU 50.

Computers 60, for example personal computers located on a residential premises, are connected by connections 62 to LAN interface 20 by LAN connectors 63. LAN connectors 63 are preferably RJ-45 jacks, but any suitable computer connection means may be employed, for example a universal serial bus (USB). While two computers are shown in Fig. 1, any desirable number of computers may be employed provided that the equipment is appropriately sized to accommodate that number.

5 LAN interface 20 further comprises a LAN physical layer (PHY) device 65 connected to LAN connectors 63 by connections 64. LAN PHY device 65 is 10 preferably an Ethernet PHY, and more specifically a MII transceiver. CPU 50 contains an Ethernet control module 67, which serves as a media access control (MAC) device and is connected to LAN PHY device 65 by connection 66. The Ethernet PHY and MAC devices support a 10/100 Mbit industry standard MII 15 Ethernet connection.

15 The CO 30 is connected by connection 72 to WAN interface 25 by WAN connector 73. WAN connector 73 is preferably a RJ-11 jack for ADSL. WAN interface 25 is preferably a universal, xDSL interface, and more preferably an ATM over ADSL interface. xDSL is a generic term encompassing all known varieties of digital subscriber lines (DSL) such as ADSL, U-ADSL, SDSL, HDSL2, 20 RADSL, etc. Other types of WAN interfaces could be used such as T1, cable modem, wireless broadband modem, or ATM-25.

20 WAN interface 25 further comprises WAN controller 75 connected to WAN connector 73 by connection 74. WAN controller 75 is connected to ATM control

module 55 by connection 76. WAN controller 75 is preferably Globespan's G7000 multimode chip set, which further comprises an analog front end (AFE) chip (Globespan GS707X) and a multimode xDSL downloadable DSP and framer chip (Globespan GS707X). The AFE chip provides analog to digital and digital to chip (Globespan GS707X). The AFE chip provides analog to digital and digital to 5 analog signal conversions. The DSP and framer chip provides digital signal processing and signal transmission framing functions.

POTS interface 15, LAN interface 20, WAN interface 25, CPU 50, and ATM controller 55 are mounted on motherboard 80, which is a printed circuit board (also referred to as a logic board) that provides the various connections 10 between ISH components as discussed above. The motherboard may include expansion slots to accept expansion cards such as an additional POTS card, an ATM-25 interface, a cable modem, etc. Motherboard 80 is connected to power supply 85 and battery pack 90, thereby providing power to the ISH components, the attached analog telephones, and the battery monitoring and charge circuitry. 15 CPU 50 contains a power supply control module 83 connected to power supply 85 by connection 84. A preferred power supply is a universal-input (40-400Hz, 90-270V AC RMS) switch mode lead/acid battery float charger with a current-limited output of nominally 13.8 V, and provides charging current to battery pack 90 as represented by connection 86. A preferred battery is a 12 volt DC, 2.5A 20 gel cell (lead-acid) battery, and preferably battery pack 90 comprises two such batteries housed within the ISH.

The power supply is plugged into a standard electrical outlet 87 and serves as the primary power source for the ISH. In the event of a power failure

to the electrical outlet, the ISH operates under backup power provided by its battery pack and basic telephone services remain available to the customer for emergency calls.

Each of the controllers illustrated as being part of the CPU 50 are actually software stored as configuration files in RAM and as binary code in flash memory

5 54. At the time of manufacture of the ISH, its final installation location and configuration are not known and therefore the required configuration files and binary code cannot be known. Instead, the ISH is manufactured with code to perform an automatic downloading of configuration files and binary code

10 according to the steps illustrated in Figure 2.

At step 100 shown in Figure 2, the system sync function is triggered by the start up of the system. Start up means that the system is installed and has power turned on. Start up occurs upon initial installation of the system, but also occurs each time that power to the system is turned off, e.g. when power at outlet 15 87 is lost for sufficient time to deplete the backup power supply battery pack 90. In step 102, the system issues a DHCP, dynamic host control protocol, request to the CO 30. In response to this request, the CO DHCP server provides the IHS with a unique IP address to identify the ISH itself. The DHCP server also supplies the name of the appropriate configuration file, a list of domain names of 20 configuration file servers where the configuration file is stored and a list of IP addresses for domain name servers which can provide IP addresses for the configuration file servers. In the preferred embodiment, the configuration file servers are standard TFTP, trivial file transfer protocol, servers.

Since the ISH now has its own IP address, it can send data packets to and receive data packets from any other site on the Internet. In step 104, the ISH first sends a message to a domain name server to obtain the IP addresses of the configuration file servers. It then requests the configuration file from a configuration file server. In step 106, the ISH receives and parses the configuration file. The configuration file contains the name of the appropriate binary file. At the time of manufacture, a basic binary file is loaded into flash memory 54, since otherwise the system cannot start up. But the basic binary file is not suitable for actually running the ISH after it is installed. As part of the parsing process, the ISH checks the binary file name in the configuration file against the name in the flash memory 54. On initial start up, it will not match.

In step 108, the ISH sends another message to the configuration file server requesting the binary file called for in the configuration file. When the new binary file is received, it is stored in flash memory in step 110. In the preferred embodiment, flash memory 54 has spaces allocated to store two binary file images. At the time the ISH is manufactured only one, the basic file, is stored in the first space. When the new binary file is received, it is stored in the second. If the binary file is later replaced with an improved version, it will be written over the first space. Later improved versions are written in space two, then space one, and so on. This process avoids loss of all valid binary files in the event of power loss or other interruption during the process of writing a new binary file to flash memory.

When the ISH started up in step 100, it used the original binary file stored in flash memory 54. The CPU read that binary file from the flash memory 54 and stored it in RAM for use in actual operation of CPU 50. It continues to operate with that version until the copy in RAM is overwritten with new binary code. So, 5 when step 110 is completed, the ISH issues a reset command. When the CPU 50 resets, it loads the new binary file from flash memory 54 into RAM. The reset command also causes the ISH to start over at step 104 where it obtains the configuration file again. When the ISH parses the configuration file at step 106, it will find that the name of the binary file in the configuration file now matches the 10 name of the binary file which it is running. It will then transition directly to the running state at step 112, as indicated by the path 114. Once in the running state, the ISH can operate properly until power loss or some error or an intentional reset command causes the system to reset. The configuration file which was downloaded provides the information needed by the CPU 50 to 15 properly operate the various components and interfaces of the ISH described above.

The above description assumes that each step of the process works as intended on the first try. The ISH supervisor provides some additional steps when the process does not work smoothly. One is an overall timeout for the 20 startup process. If the ISH does not reach the running state 112 or an error state 116 within the set time, the ISH will reset and start the process over. In each of steps 104 and 108, there are provided timeouts which cause the ISH to repeat the step if the requested file is not received within the preselected time. There is

also a limit on the number of retry attempts which can be made before the system tries another domain name server or configuration file server. At step 102, the DHCP response provided a list of IP addresses for domain name servers and a list of domain names for configuration file servers. In step 104, if 5 the ISH cannot contact the first domain name server in the set number of retry attempts, it tries the second address, then the third, etc. It will cycle through the list until it succeeds in obtaining a list of IP addresses for the domain name servers. In steps 104 and 108, if the IHS cannot contact the first IP address of a domain name server in the set number of retry attempts, it tries the second, then 10 the third, etc. It will cycle through the list until it succeeds in obtaining the configuration file in step 104 or the binary file in step 108. If the parsing operation in step 106 fails, the system also goes to the error state. Upon entering the error state 116, the system resets and starts the process over.

While the present invention has been illustrated and described with 15 reference to particular equipment and methods of operation, it is apparent that various changes and substitution of equivalent parts can be made within the scope of the present invention as defined by the appended claims.